

### **ABSTRACT OF THE DISCLOSURE**

Disclosed is a PDP driving method. During the reset period, a bias voltage of a sustain electrode below a first bias voltage is applied during a peak maintain period after a Y ramp falling period, or a discharge stabilization stage for lowering a relative potential of a scan

5 electrode is provided before the Y ramp starts falling, thereby improving address features, stably obtaining voltage margins, providing advantages for low gray and low temperatures according to stable obtainment of the voltage margins, and reducing the light in the reset period to improve the contrast.

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